PTO/SB/08a (11-08)
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Su	ubstitute for form 1449/PTO			Complete if Known		
				Application Number	10/698,805-Conf. #2494	
H	NFORMATION	1 DI	SCLOSURE	Filing Date	October 31, 2003	
S	STATEMENT E	3Y /	APPLICANT	First Named Inventor	Peter SZPAK	
(Use as many sheets as necessary)				Art Unit	2123	
				Examiner Name	J. S. Proctor	
Sheet	, 1	of	. 2	Attorney Docket Number	MWS-058RCE	

	U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Document Number  Number-Kind Code <sup>2</sup> ( if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
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	FOREIGN PATENT DOCUMENTS									
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document  Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T®				

Examiner	Date	<u> </u>
Signature	Considered	

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Express Mail Label No. EM 192977720 US Dated: December 12, 2008

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II.	<b>IFORMATIO</b>	N DIS	SCLOSURE	Filing Date	October 31, 2003	
l s	TATEMENT	BY A	PPLICANT	First Named Inventor	Peter SZPAK	
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Sheet	2	of	2	Attorney Docket Number	MWS-058RCE	

NON PATENT LITERATURE DOCUMENTS						
Examiner Initials	the item (book manazine journal serial symposium catalog etc.) date nage(s) volume-it					
	C1	The MathWorks, "Simulink® Model-Based and System-Based Design, Using Simulink, Version 5," The MathWorks, Inc., (2002)				
	C2	Xilinx, Inc., "Model Sim XE User's Manual, Version 5.3, The Ultimate Tool for VHDL or Verilog Simulation," Xilinx, Inc., (2000)				
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